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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10674642
Filing Date	2003-09-30
First Named Inventor	Levine et al.
Art Unit	2192
Examiner Name	Chou, Andrew Y.
Attorney Docket Number	AUS920030484US1

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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	5761103		1998-06-02	Oakland et al.	
	2	6549959	B1	2003-04-13	Yates et al.	
	3	6772322	B1	2004-08-03	Merchant et al.	
	4	5937437		1999-08-10	Roth et al.	
	5	6070009		2000-05-30	Dean et al.	
	6	6381679	B1	2002-04-30	Matsubara et al.	
	7	6687794	B2	2004-02-03	Malik	
	8	6539458	B2	2003-03-25	Holmberg	

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	9	6973542	B1	2005-12-06	Schmuck et al.	
	10	6782454	B1	2004-08-24	Damron	
	11	5652858		1997-06-29	Okada et al.	
	12	6848029	B2	2005-01-25	Coldewey	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i>	Application Number	10674642
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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T5
	1	"Method for the dynamic prediction of nonsequential memory accesses", September 25, 2002, pp. 1-4, ip.com IPCOM000009888D	<input type="checkbox"/>
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See attached certification statement.
 Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
 None

SIGNATURE

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Signature	/Gerald H. Glanzman/	Date (YYYY-MM-DD)	2006-11-10
Name/Print	Gerald H. Glanzman	Registration Number	25035

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